

PATENT ATTORNEY DOCKET NO. 040879-5092 U.S. Application No. 10/082,174

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Stanford W. CRANE, Jr. et al.)
U.S. Application No.: 10/082,174) Group Art Unit: 2814
Filed:	February 26, 2002	_) Examiner: H.B. Trinh
For:	MICRO GRID ARRAY SEMICONDUCTOR DIE PACKAGE)))
Commissioner for Potents			

Commissioner for Patents Washington, D.C. 20231

Sir:

RESPONSE TO ELECTION/RESTRICTION REQUIREMENT

In response to the Election/Restriction Requirement dated December 3, 2002 (Paper No. 3), the period for response to which extends through January 3, 2003, Applicants hereby elect Group I (claims 1-11) with traverse for examination.

Applicants traverse the rejection for the following reasons. The Office Action justifies the restriction on the grounds that the product can be made with another materially different process.

However, the alleged "materially different process" is not excluded from coverage under the claims of Group II. Accordingly, the Office Action does not properly justify the restriction. Moreover, examination of all of the pending claims would not be an excessive burden on the examiner. For at least these reasons, reconsideration and withdrawal of the restriction requirement is requested.

Applicants respectfully request formal examination of this application.

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Except for issue fees payable under 37 C.F.R. § 1.18, the Commissioner is hereby authorized by this paper to charge any additional fees during the entire pendency of this application including fees due under 37 C.F.R. §§ 1.16 and 1.17 which may be required, including any required extension of time fees, or credit any overpayment to Deposit Account 50-0310. This paragraph is intended to be a **CONSTRUCTIVE PETITION FOR EXTENSION OF TIME** in accordance with 37 C.F.R. § 1.136(a)(3).

Respectfully submitted,

MORGAN, LEWIS & BOCKIUS LLP

Date: December 18, 2002

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